

≤ 2

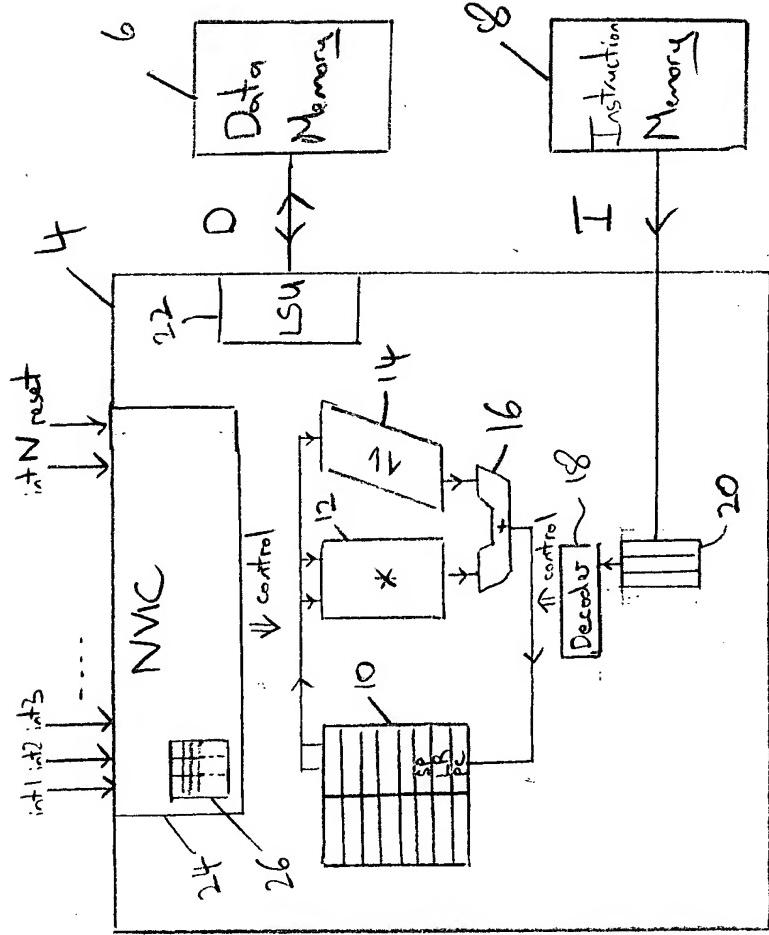


Fig. 1

NVIC Control Table

	Pending	Active	Priority	
			pre-emption	ordinary
Int 0		1		↔
Int 1				↓
Int 2	1			↓
Int 3		1		↓
⋮				

A timing diagram below the table shows two vertical spikes. The first spike is at time 26, corresponding to Int 0. The second spike is at time 30, corresponding to Int 3. The horizontal axis is labeled with 28 and 30.

Fig. 2

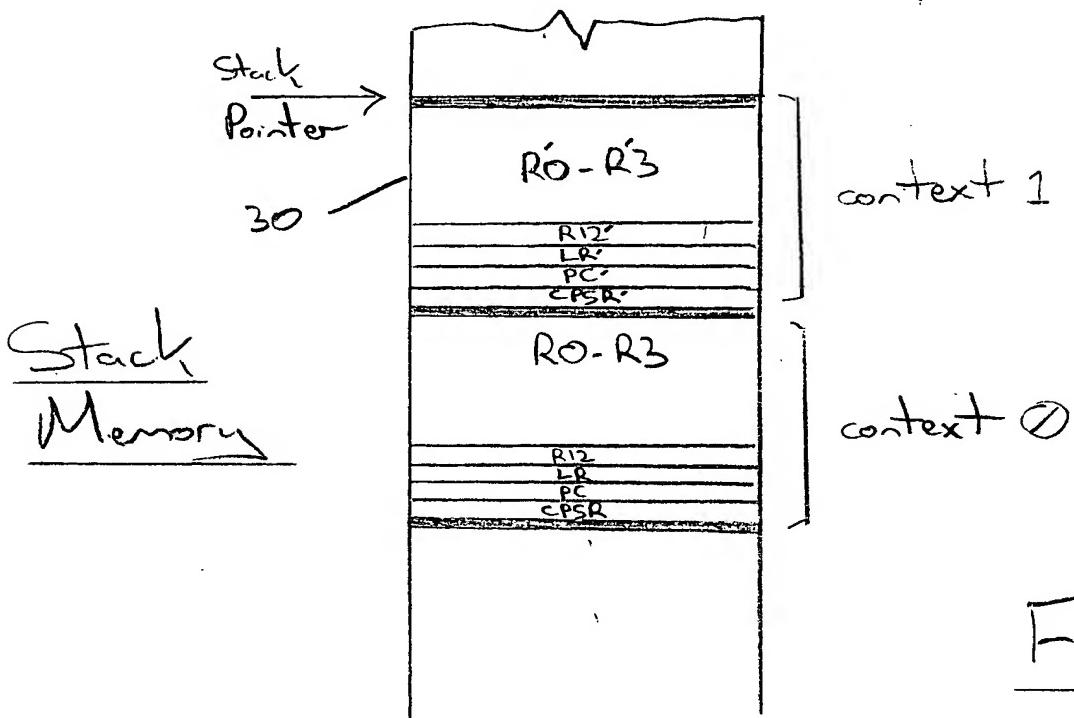


Fig. 3

Priority Inversions

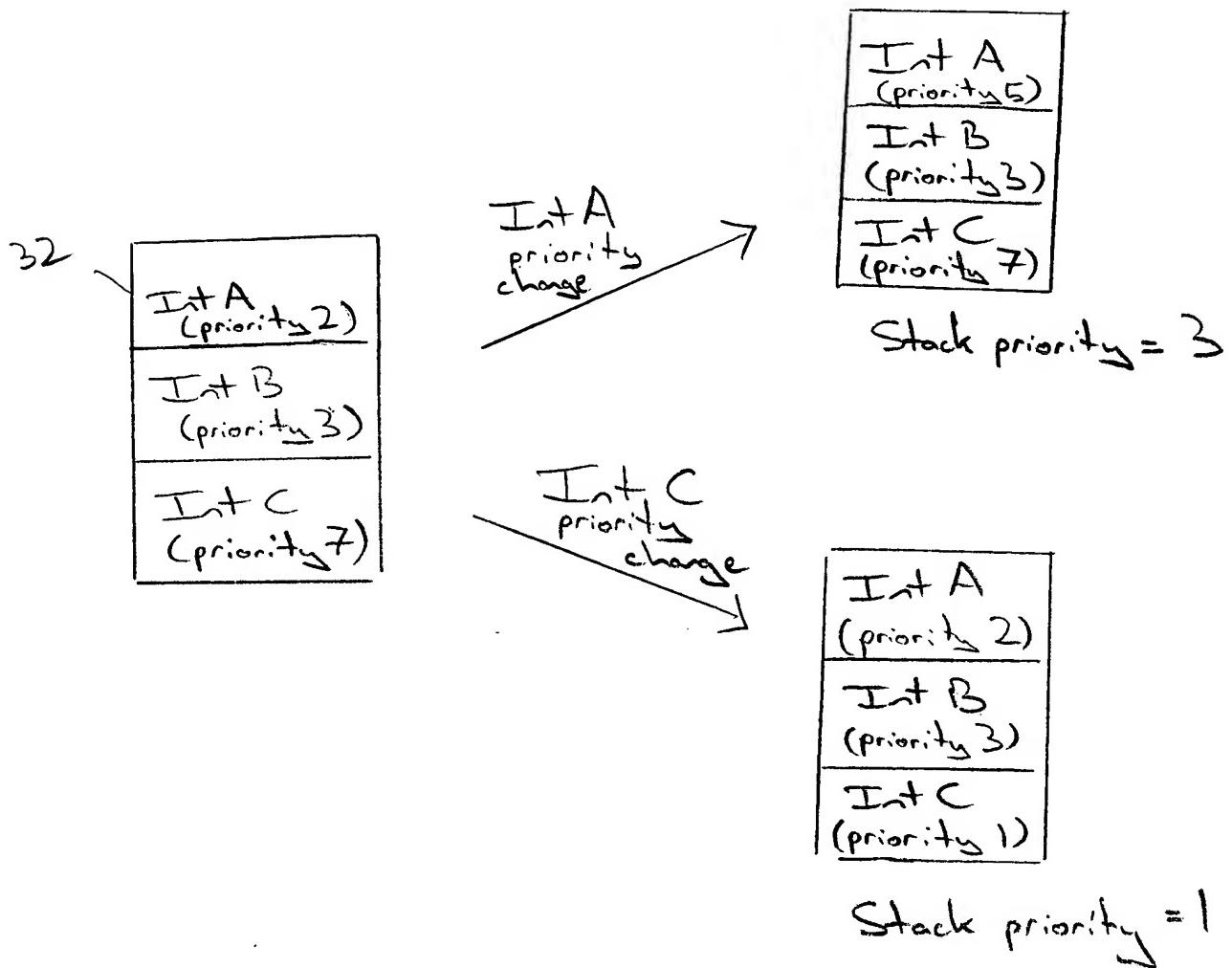


Fig. 4

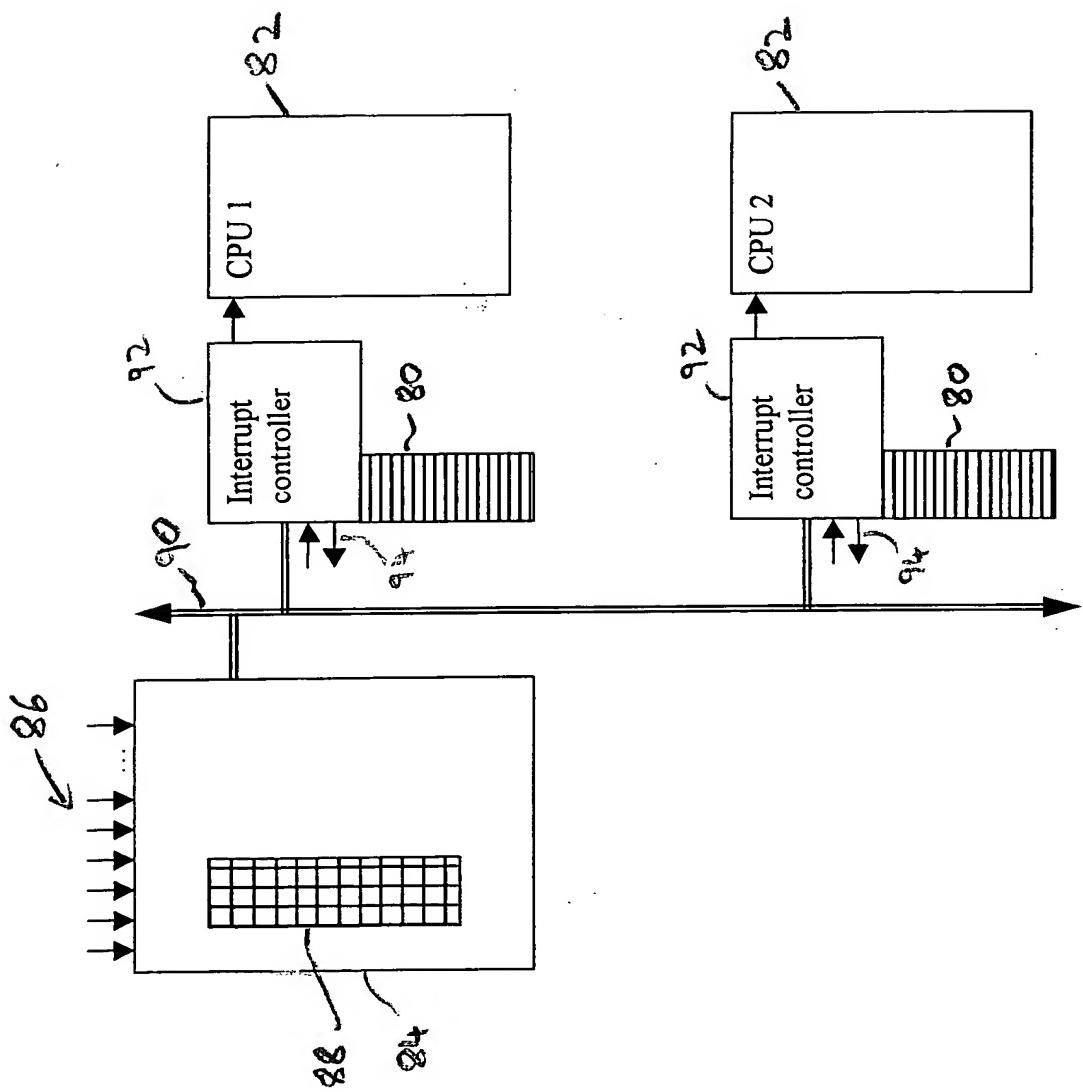


Fig. 5

Main Flow

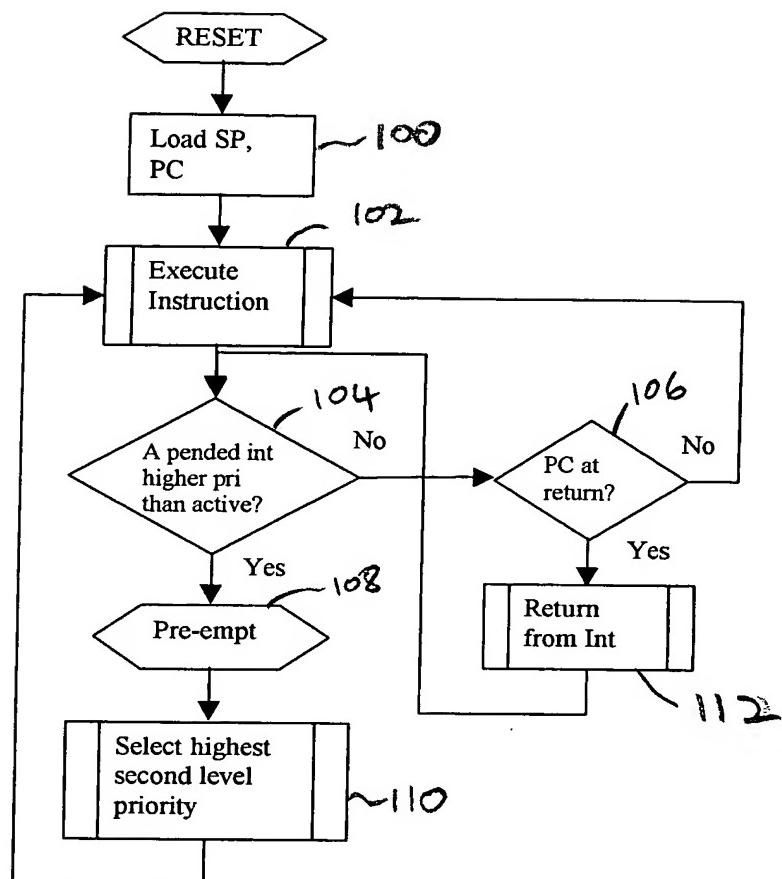


Fig. 6

Main Flow with Tail Chaining

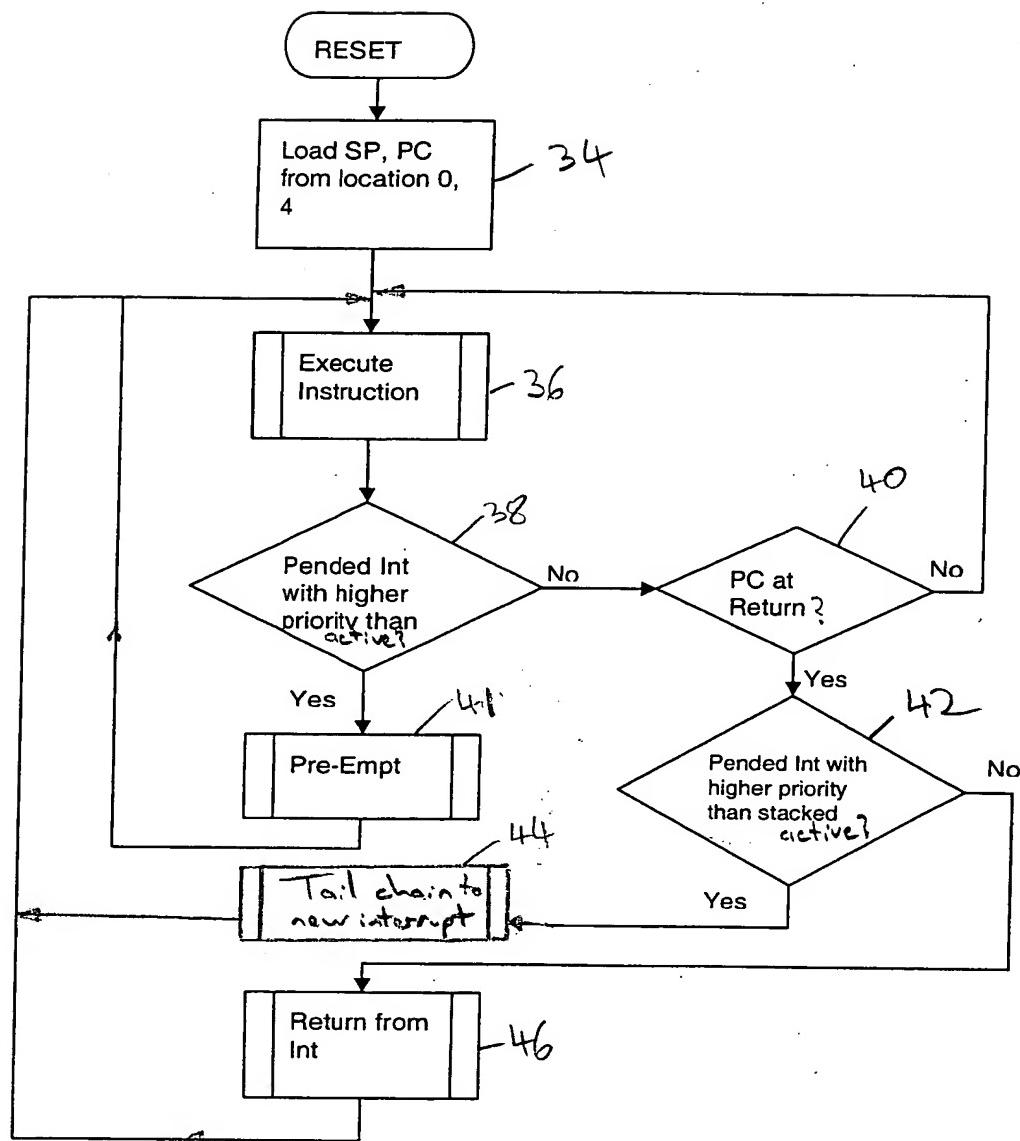


Fig. 7

Pre-emption Flow

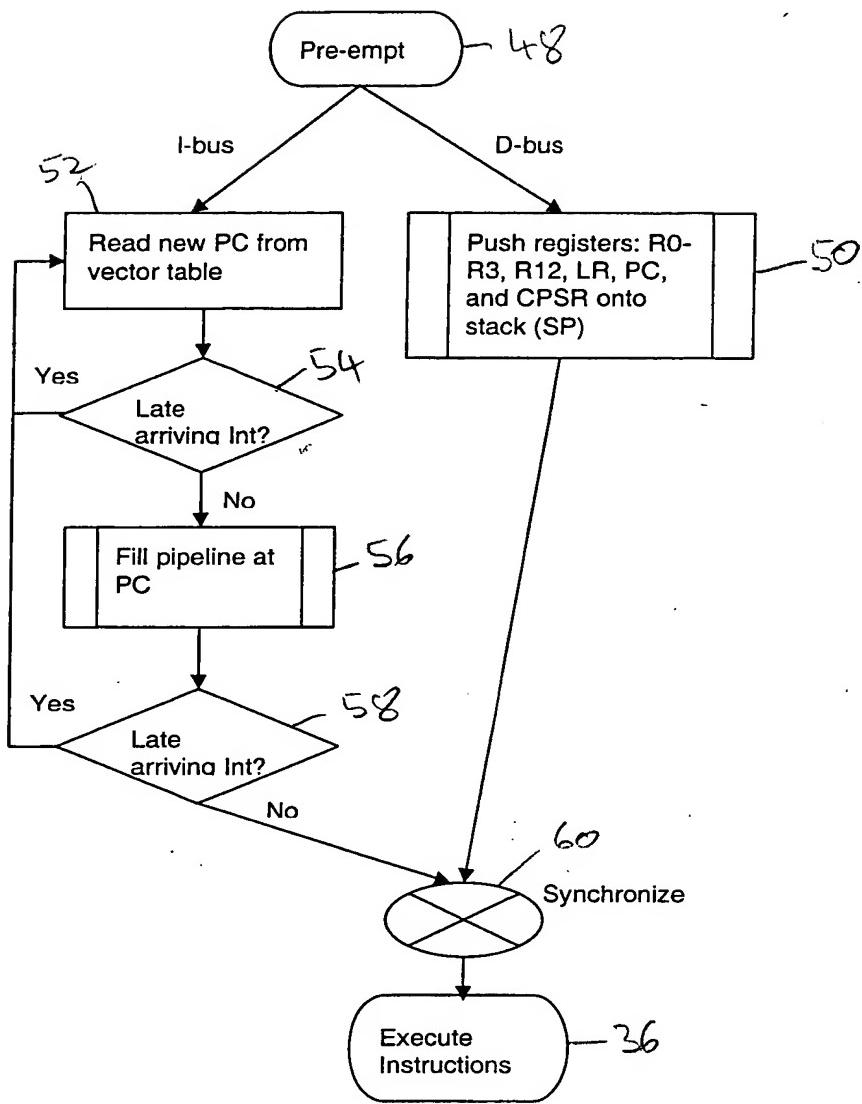


Fig. 8

Return From Interrupt Flow

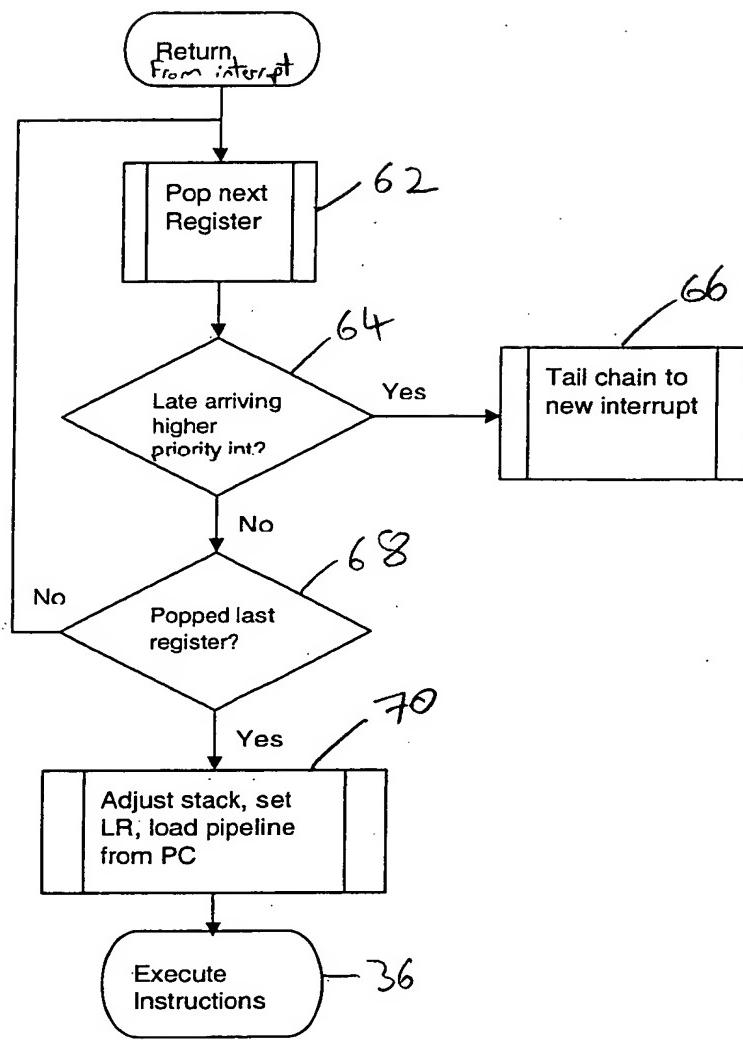
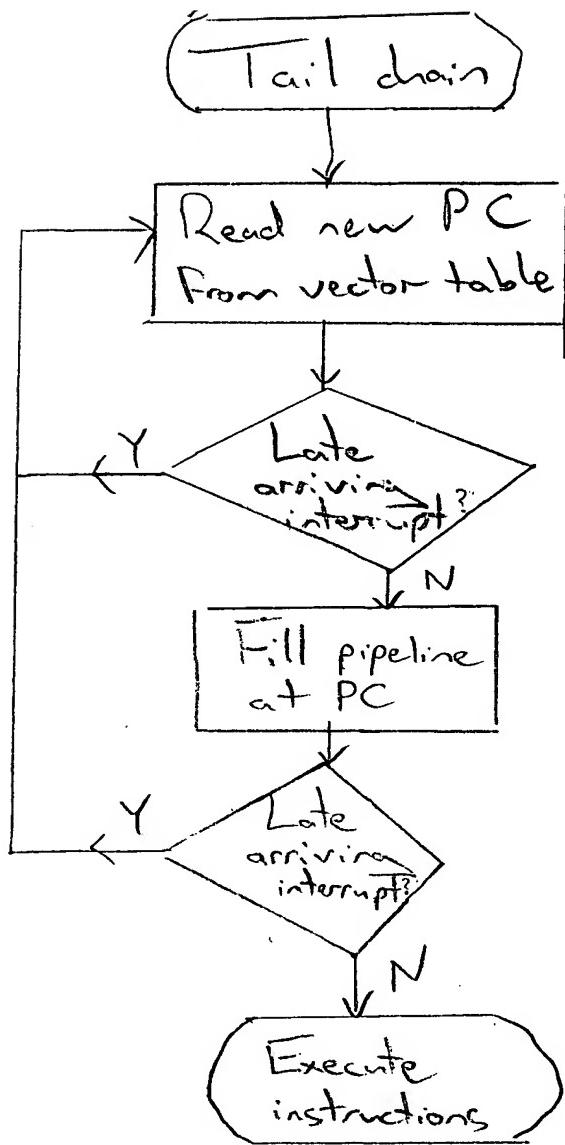


Fig. 9

Tail Chaining Flow



11

Fig. 10